



Electronic Filing System (EFS) Data

Electronic Patent Application Submission

USPTO Use Only

EFS ID: 49839

Application ID: 10628996



Title of Invention: BIST CIRCUIT FOR MEASURING
PATH DELAY IN AN IC

First Named Inventor: Olivier Ganry

Domestic/Foreign Application: Domestic Application

Filing Date: 2003-10-27

Effective Receipt Date: 2003-10-28

Submission Type: Information Disclosure
Statement

Filing Type:

Confirmation number: 7336

Attorney Docket Number: CRED 2617

Total Fees Authorized:

Digital Certificate Holder: cn=John Smith-Hill,ou=Registered Attorneys,ou=Patent and Trademark
Office,ou=Department of Commerce,o=U.S. Government,c=US

Certificate Message Digest: e94fb248dd04584fc72e9e2e31876f8b85707505



TRANSMITTAL

Electronic Version v1.1
Stylesheet Version v1.1.0

Title of Invention	BIST CIRCUIT FOR MEASURING PATH DELAY IN AN IC	
Application Number:	10/628996	
Date:	2003-10-27	
First Named Applicant:	Olivier Ganny	
Confirmation Number:	7336	
Attorney Docket Number:	CRED 2617	
<p>I hereby certify that the use of this system is for OFFICIAL correspondence between patent applicants or their representatives and the USPTO. Fraudulent or other use besides the filing of official correspondence by authorized parties is strictly prohibited, and subject to a fine and/or imprisonment under applicable law.</p> <p>I, the undersigned, certify that I have viewed a display of document(s) being electronically submitted to the United States Patent and Trademark Office, using either the USPTO provided style sheet or software, and that this is the document(s) I intend for initiation or further prosecution of a patent application noted in the submission. This document(s) will become part of the official electronic record at the USPTO.</p>		
Submitted by:	Elec. Sign.	Sign. Capacity
John Smith-Hill Registered Number: 27,730	/John SMITH-HILL/	Attorney
Documents being submitted us-ids	Files 2617-usidst.xml us-ids.dtd us-ids.xsl	
Comments		



ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18
Stylesheet Version v18.0

Title of
Invention

BIST CIRCUIT FOR MEASURING PATH DELAY IN AN IC

Application Number: 10/628996



Confirmation Number: 7336

First Named Applicant: Olivier Ganny

Attorney Docket Number: CRED 2617

Art Unit: 2133

Search string: (4875201 or 5923676 or 6058496).pn.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	4875201	1989-10-17	Dalzell			
	2	5923676	1999-07-13	Sunter et al			
	3	6058496	2000-05-02	Gillis et al			

Signature

Examiner Name

Date